



PATENT Customer No. 22,852 Attorney Docket No. 04329.3181

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)
Kiyotaka MIYANO	) Group Art Unit: 2826
Application No.: 10/706,034	) Examiner: Erdem, Fazli
Filed: November 13, 2003	)
For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME	) ) )
Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	
Sir:	

## RESPONSE TO RESTRICTION REQUIREMENT

In the Restriction Requirement mailed August 10, 2004, with a period for response extending to September 10, 2004, the Examiner required restriction under 35 U.S.C. § 121 between Group I (claims 1 - 4) and Group II (claims 5 - 20). Applicant provisionally elects to prosecute Group II, claims 5 - 20, characterized by the Examiner as drawn to a method of making a semiconductor device, without traverse.

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Dated: September 9, 2004

David M. Longo Reg. No. 53,235